

LAST - [04/30/01]

File View Edit Tools Window Help

Drafts
Pending
Active
L1: (8) (((temporal adj5 logic) and (state adj3 machine) and (condition\$3 near9 transitio
L3: (7) 11 and 12
L4: (54126) state near9 test\$4
L2: (71001) event near9 operat\$4
L5: (1) 13 and 14
L6: (115294) (event or tempora\$3 or logic or transit\$4 or state or count\$4) near9 test\$4
L7: (7) 13 and 16
L8: (1) 6581191.pn. and 16
Failed
Saved

	u	1	Document ID	Title	Cu
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040025122	2 Hardware-based HDL code coverage and design analysis	710
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030182642	2 Hardware debugging in a hardware description language	710
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030131325	2 Method and user interface for debugging an electronic system	710
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20030069724	2 Method and system for debugging an electronic system using instrument	703
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030046658	2 Event-based temporal logic	711
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6618839 B1	2 Method and system for providing an electronic system design with enha	710
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6581191 B1	2 Hardware debugging in a hardware description language	710

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